

IN THE CLAIMS

The following are Claims 1-28.

1. (currently amended) An automatic gain control system comprising:

an automatic gain control core circuit adapted to apply a gain to an input signal to provide an output signal;

a power detector circuit adapted to receive the output signal and provide a first signal which indicates a power level of the output signal; and

a processor adapted to control the gain of the automatic gain control core circuit based on the first signal, wherein the processor provides a calibration signal to the power detector circuit to calibrate a reference level for the system.

2. (original) The system of Claim 1, further comprising an analog-to-digital converter adapted to receive the first signal from the power detector circuit and provide the first signal as a digital signal to the processor.

3. (canceled)

LAW OFFICES OF
MACPHERSON KWOX
CHEN & TIEB LLP

2400 Michelson Drive
Suite 210
Irvine, CA 92612
(949) 752-7040
FAX (949) 752-7049

3/4. (currently amended) The system of Claim 1 3, further comprising a digital-to-analog converter adapted to receive the calibration signal and provide the calibration signal as an analog signal to the power detector circuit.

4/5. (currently amended) The system of Claim 1 3, further comprising:

PN a first switch, coupled between the processor and the power detector circuit, adapted to be closed by the processor during a calibration mode of the system to calibrate the reference level; and

a second switch, coupled between the automatic gain control circuit and the power detector circuit, adapted to be closed by the processor during a continuous automatic gain control mode of the system.

9/8. (original) The system of Claim 1, wherein the power detector circuit comprises:

a correlator; and

a low pass filter coupled to the correlator to determine the power level of the output signal.

LAW OFFICES OF
MACPHERSON KIVOK
CHEN & HED LLP

2400 Michelson Drive
SUITE 210
Irvine, CA 92613
(949) 133-7040
FAX (949) 732-1069

⁶~~7~~. (original) The system of Claim 1, wherein the processor provides a coarse gain control signal and a fine gain control signal to the automatic gain control core circuit to control the gain.

⁷~~8~~. (original) The system of Claim ⁶~~7~~, wherein the automatic gain control core circuit comprises a plurality of gain stages, with each of the gain stages having a plurality of transconductance stages.

PN ⁸~~9~~. (original) The system of Claim ⁷~~8~~, wherein the fine gain control signal controls a bias current value for the transconductance stages, and the coarse gain control signal selects which of the transconductance stages contribute to the gain.

⁹~~10~~. (original) The system of Claim ⁷~~8~~, wherein the plurality of transconductance stages for each gain stage is associated with at least one load impedance.

¹⁰~~11~~. (original) The system of Claim ⁹~~10~~, wherein the load impedance comprises a shunt, a shunt-series, a series-shunt, a series-shunt-series, a T-coil, a T-coil with a cross-coupled capacitor, or a series-T-coil.

LAW OFFICES OF
MACPHERSON KWOK
COEN & HED LLP

2022 Mission Drive
Suite 210
Irvine, CA 92613
(949) 752-7049
FAX (949) 752-7049

¹¹
~~12~~. (original) An automatic gain control circuit comprising:

an amplifier adapted to apply a gain to an input signal to provide an output signal;

a detector adapted to receive the output signal and provide a first signal based on the output signal; and

a processor adapted to provide a coarse gain control signal and a fine gain control signal to the amplifier based on the first signal to control the gain of the amplifier, wherein the processor determines a reference level value for the output signal by providing a calibration signal to the detector and setting the reference level value based on the first signal.

¹²
~~13~~. (original) The circuit of Claim ¹¹~~12~~, wherein the detector is a power detector and the first signal is based on an average power level of the output signal.

¹³
~~14~~. (original) The circuit of Claim ¹¹~~12~~, wherein the detector is a peak detector and the first signal is based on a peak amplitude level of the output signal.

LAW OFFICES OF
MACPHERSON RYAN
CIEP & BIRD LLP

2401 Mendenhall Drive
SUITE 210
IRVINE, CA 92614
(949) 753-7040
FAX (949) 753-7049

¹⁴
~~15.~~ (original) The circuit of Claim ¹¹~~12~~, further comprising:

a digital-to-analog converter adapted to receive the calibration signal and provide the calibration signal as an analog signal to the detector; and

an analog-to-digital converter adapted to receive the first signal from the detector and provide the first signal as a digital signal to the processor.

PN ¹⁵
~~16.~~ (original) The circuit of Claim ¹¹~~12~~, wherein the detector is a power detector comprising a low pass filter coupled to a correlator.

¹⁶
~~17.~~ (original) The circuit of Claim ¹¹~~12~~, wherein the fine gain control signal is set to minimize an absolute value of the first signal minus a reference value.

¹⁷
~~18.~~ (original) The circuit of Claim ¹¹~~12~~, wherein the amplifier comprises a gain stage, with the gain stage having a plurality of transconductance stages, wherein the fine gain control signal controls a bias current value for the transconductance stages and the coarse gain control signal controls which of the transconductance stages contribute to the gain of the amplifier.

LAW OFFICES OF
MACNEILSON KWOK
CHEN & CHOI LLP

2402 Michelson Drive
SUITE 210
Irvine, CA 92613
(949) 753-3040
FAX (949) 753-7049

¹⁸~~19~~. (original) The circuit of Claim ¹⁷~~18~~, wherein the plurality of transconductance stages are associated with at least one load impedance.

¹⁹~~20~~. (original) The circuit of Claim ¹⁸~~19~~, wherein the load impedance comprises a shunt, a shunt-series, a series-shunt, a series-shunt-series, a T-coil, a T-coil with a cross-coupled capacitor, or a series-T-coil.

²⁰~~21~~. (currently amended) A method of providing automatic gain control, the method comprising:

PN providing a gain to an input signal to provide an output signal;

monitoring a power level of the output signal;

providing a calibration signal to the monitoring of the power level to calibrate a reference level for the automatic gain control; and

providing a coarse gain control and a fine gain control to control the gain based on the monitoring to maintain the output signal within a desired signal level range.

²¹~~22~~. (original) The method of Claim ²⁰~~21~~, wherein the monitoring estimates an average power level of the output signal.

LAW OFFICES OF
MACPHERSON KWOK
CHEN & TSE LLP

2400 Michelson Drive
Suite 210
Irvine, CA 92612
(949) 753-1040
FAX (949) 753-7049

²²
~~23~~. (original) The method of Claim ²⁰~~21~~, further comprising calibrating the monitoring to obtain a reference level value, with the desired signal level range based on the reference level value.

²³
~~24~~. (original) The method of Claim ²⁰~~21~~, wherein the gain is performed in stages, with the coarse gain control and the fine gain control controlling a gain of each of the stages.

²⁴
~~25~~. (original) A method of calibrating and monitoring an automatic gain control circuit, the method comprising:

providing a calibration signal whose signal level is estimated to provide a reference value;

setting a range for an output signal based on the reference value;

providing a gain to an input signal to provide the output signal;

monitoring an output signal level of the output signal;
and

adjusting a coarse gain of the gain to maintain the output signal within the range.

LAW OFFICES OF
MACPHERSON KWOK
CHUN & HUI LLP

3400 Macdonald Drive
Suite 210
Fremont, CA 94538
(415) 773-7040
FAX (415) 752-7049

²⁵
~~26~~. (original) The method of Claim ²⁴~~25~~, further comprising setting a fine gain of the gain to minimize an absolute value of the power level of the output signal minus the reference value.

PN ²⁶
~~27~~. (original) The method of Claim ²⁴~~25~~, wherein the monitoring estimates an average power level of the output signal.

²⁷
~~28~~. (original) The method of Claim ²⁴~~25~~, wherein the monitoring estimates a peak amplitude signal level of the output signal.

LAW OFFICES OF
MACPHERSON KWOK
CHEN & YEID LLP

2400 Michelson Drive
SUITE 210
Irvine, CA 92614
(949) 713-7000
FAX (949) 713-7000